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Inventor Name Search Result

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Last Name = PELEG
First Name = ZOHAR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07642928	5250940	150	01/18/1991	MULTI-MODE HOME TERMINAL SYSTEM THAT UTILIZES A SINGLE EMBEDDED GENERAL PURPOSE/DSP PROCESSOR AND A SINGLE RANDOM ACCESS MEMORY	PELEG, ZOHAR
07890691	5218314	150	05/29/1992	HIGH RESOLUTION, MULTI-FREQUENCY DIGITAL PHASE-LOCKED LOOP	PELEG, ZOHAR
09460534	6507899	150	12/13/1999	INTERFACE FOR A MEMORY UNIT	PELEG, ZOHAR
10549678	Not Issued	30	09/19/2005	Method and apparatus for partitioning allocation and management of jitter buffer memory for tdm circuit emulation applications	PELEG, ZOHAR
10803900	Not Issued	41	03/19/2004	Integrated fuel cell controller for devices	PELEG, ZOHAR
11555283	Not Issued	30	11/01/2006	Asymmetric Differential Timing	PELEG, ZOHAR
60382432	Not Issued	159	05/23/2002	Methods and apparatus for handling multiple parallel processes in hardware, using a single, time-division state machine	PELEG, ZOHAR
60484294	Not Issued	159	07/03/2003	Method and apparatus for partitioning allocation and management of jitter buffer memory for TDM circuit emulation application	PELEG, ZOHAR
60734781	Not Issued	159	11/09/2005	Asymmetric differential timing	PELEG, ZOHAR

60817068	Not Issued	159	06/29/2006	Fuel cell system and method of activating the fuel cell	PELEG, ZOHAR
60953206	Not Issued	20	08/01/2007	Network Device And Method For Synchronizing Between Asynchronous	PELEG, ZOHAR
60971000	Not Issued	20	09/10/2007	TIMESTAMP SYNTHESIZER AND METHOD THEREOF	PELEG, ZOHAR

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